

Fig.1 (Prior Art)

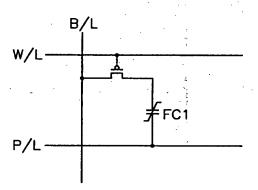


Fig.2 (Prior Art)

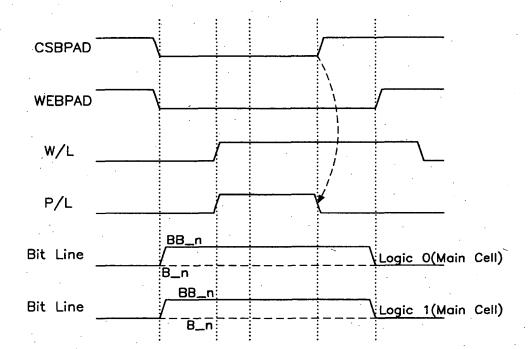


Fig.3A (Prior Art)

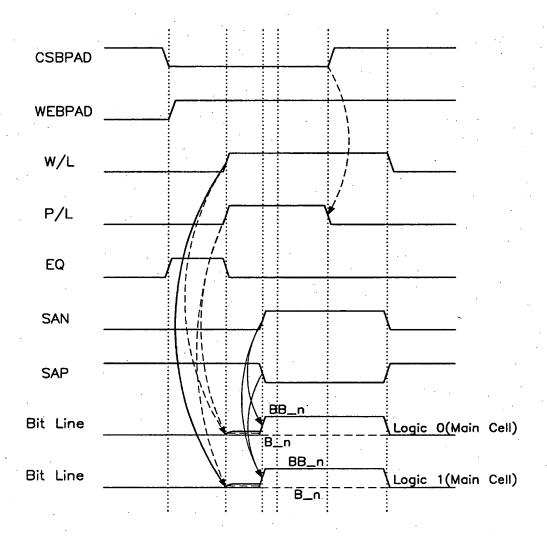


Fig.3B (Prior Art)

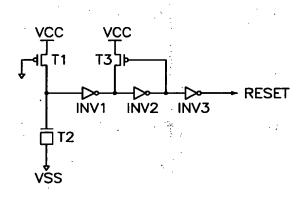


Fig.4

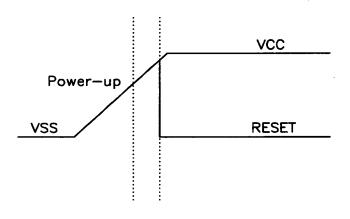


Fig.5

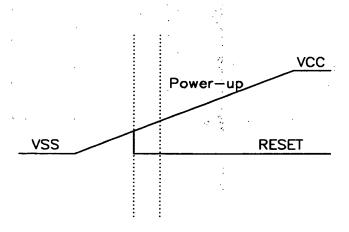


Fig.6

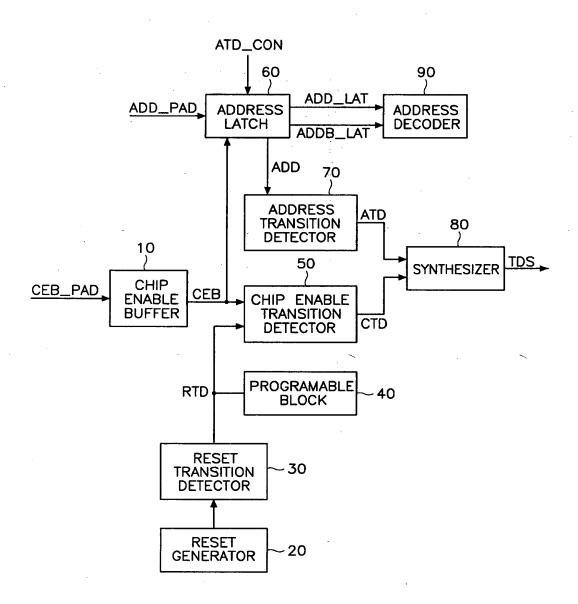


Fig.7

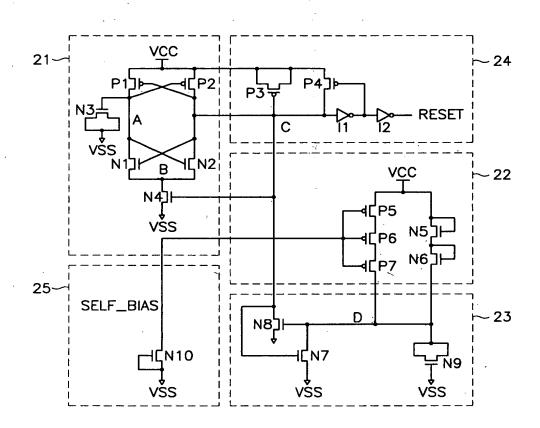


Fig.8

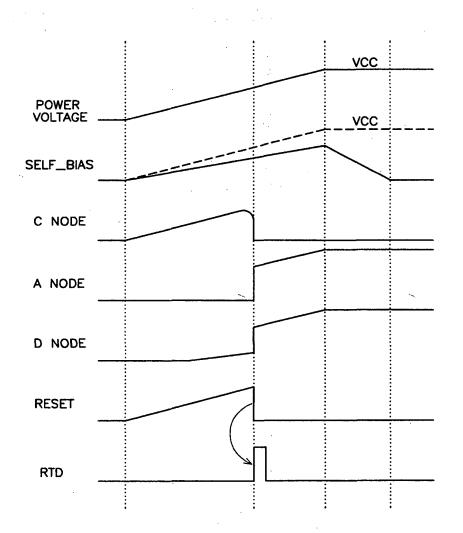


Fig.9

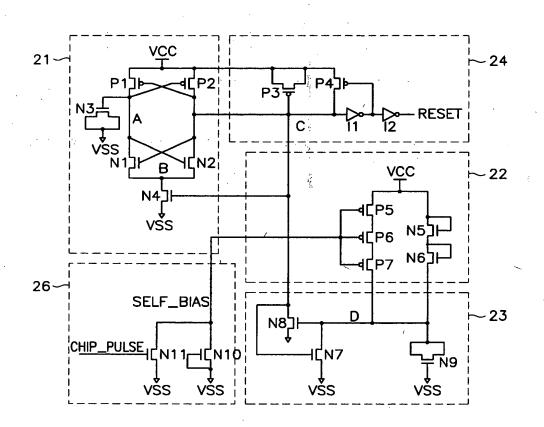


Fig.10

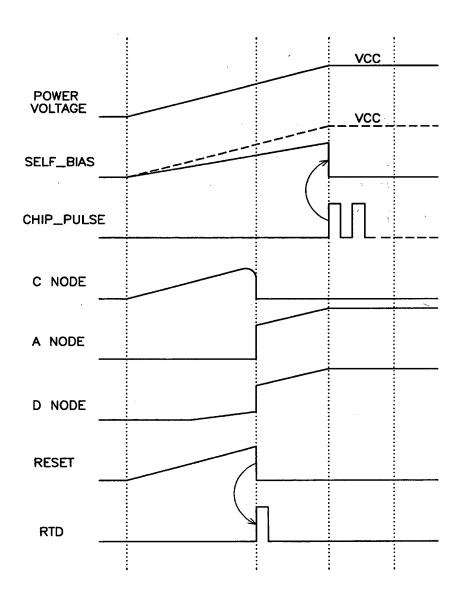


Fig. 11

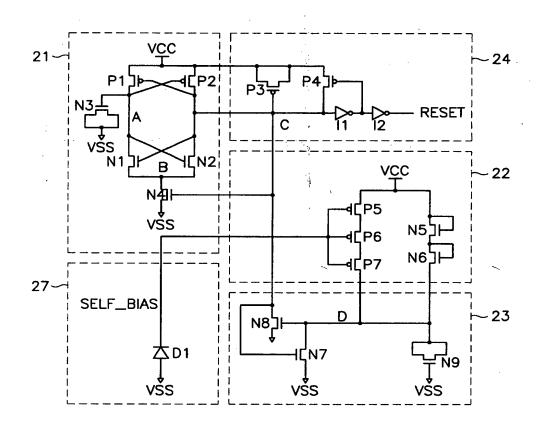


Fig.12

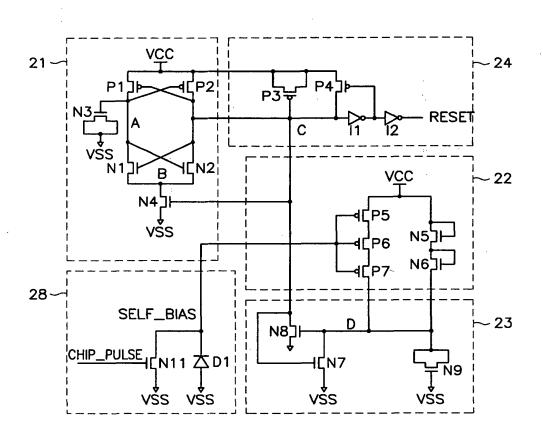


Fig.13

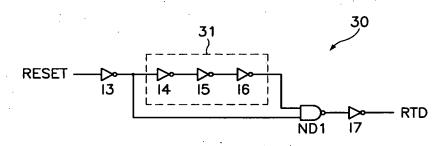


Fig.14

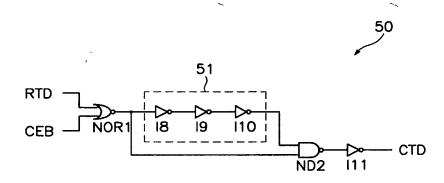


Fig.15

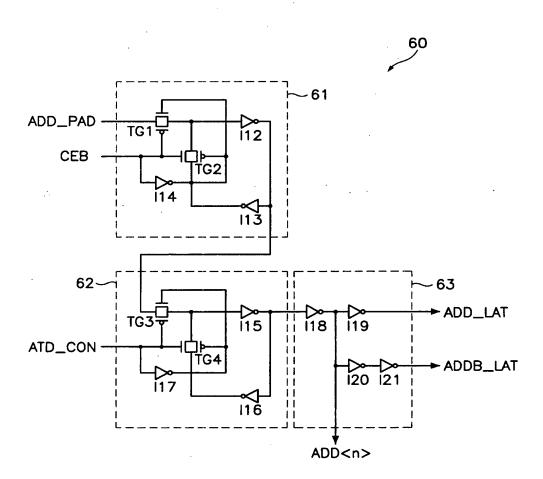


Fig.16

"Reset Circuit and Feram Using the Same"

Attorney Docket No.: 40296-0015

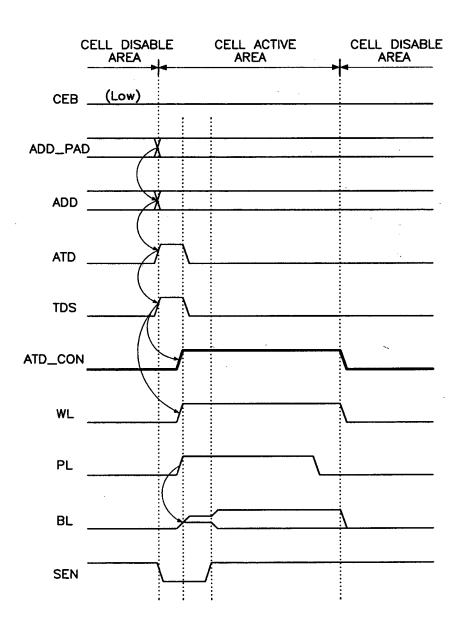


Fig.17

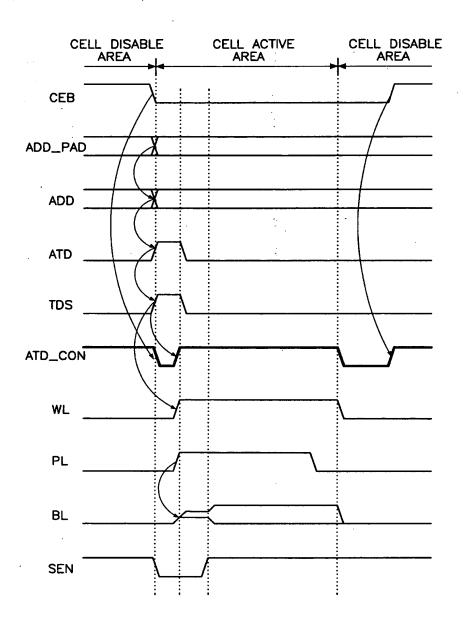


Fig.18